

Driver Installation

If you use Windows system, please install the driver first (CP2102N)

<https://bit.ly/36Qhuv2>

1. Cc2652P Firmware Flashing

1.1 Firmware acquisition

Download the Z-Stack_3.x.0 firmware of CC2652P USB Dongle from the following link:

Coordinator:

https://github.com/Koenkk/Z-Stack-firmware/tree/master/coordinator/Z-Stack_3.x.0

Router:

https://github.com/Koenkk/Z-Stack-firmware/tree/master/router/Z-Stack_3.x.0

1.2 Firmware flashing

Method 1:

Use the automatic upgrade tool "cc2538-bsl" to achieve "Auto BSL".

<https://github.com/JelmerT/cc2538-bsl>

Method 2:

CC2652P USB Dongle supports serial port Bootloader to flash firmware. Use firmware flashing tools like "Flash Programmer 2" to flash the firmware.

1. Enter the serial port Bootloader

There are two ways for Dongle to enter Bootloader:

1. Manual mode

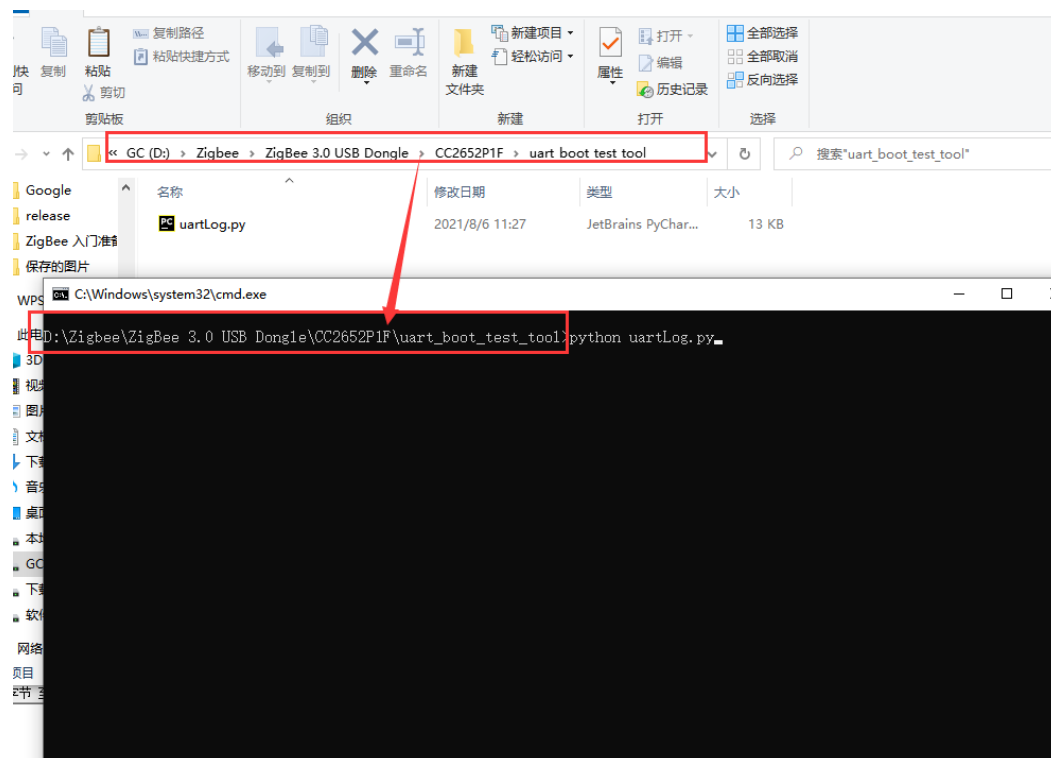
Keep pressing the Boot button, restart the device, and release the Boot button after Dongle enters the serial port Bootloader.

2. Automatically enter the serial port Bootloader through a python script

<https://bit.ly/3LeYdIO>

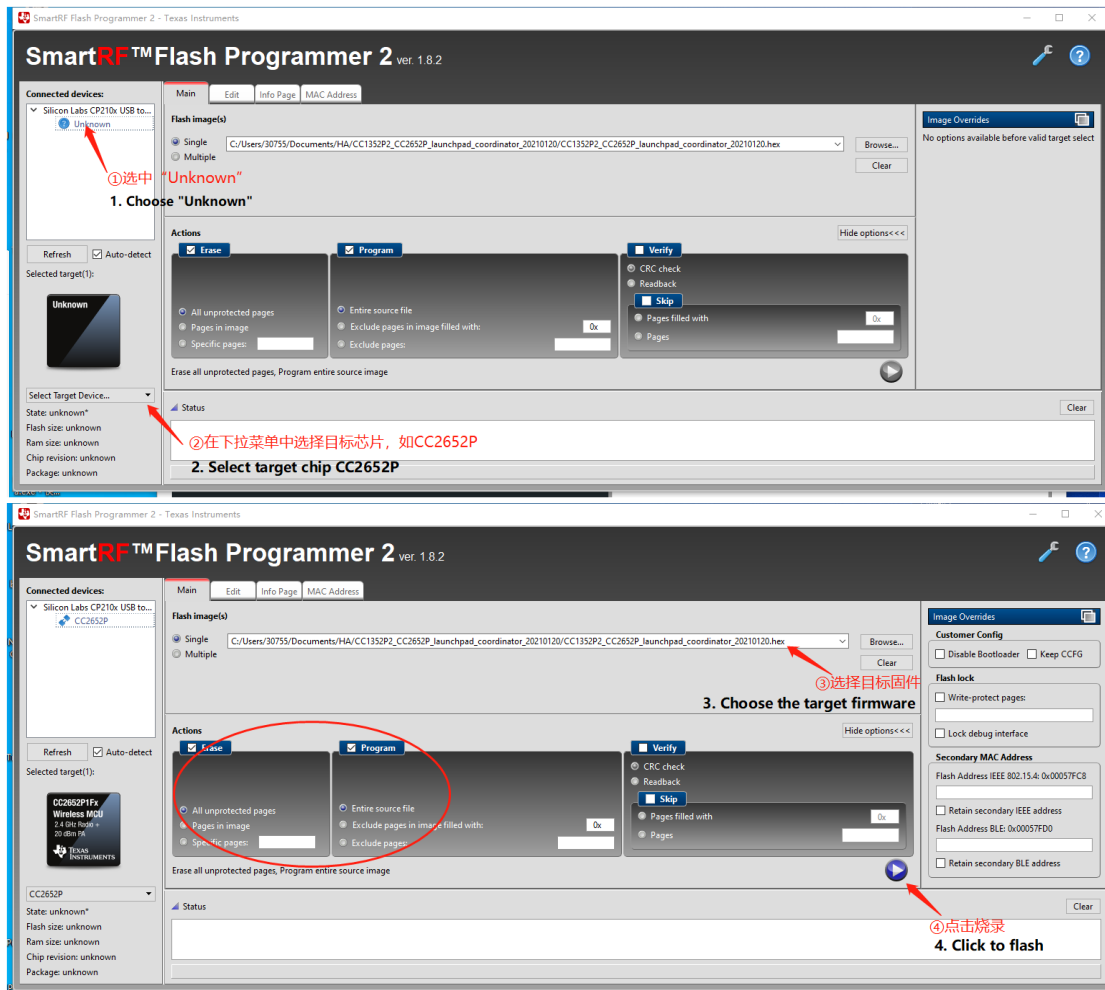
Step to execute python script:

1) Download, unzip and execute the file



2) Enter the serial port number inserted into the dongle

2. Using Flash Programmer 2 serial port flashing firmware



Note: There is no difference between the coordinator and routing firmware flashing steps.

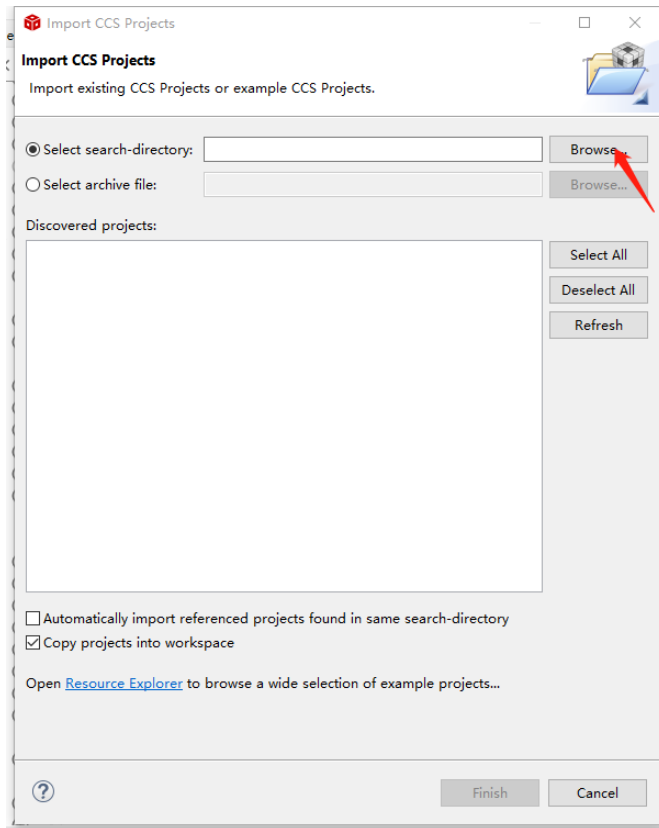
2. Enable Hardware Flow Control and Generate Corresponding Firmware (optional)

If you need to enable the hardware flow control of the CC2652P USB Dongle, you need to use CCS to import the ZNP project to configure and compile the firmware that supports the hardware flow control.

Note: At present, the device cannot be used after hardware flow control is enabled, and the open source platforms have not yet supported it.

2.1 Import the ZNP project of CC1352P into CCS

1. CCS 【Project】 - 【Import CCS Project】



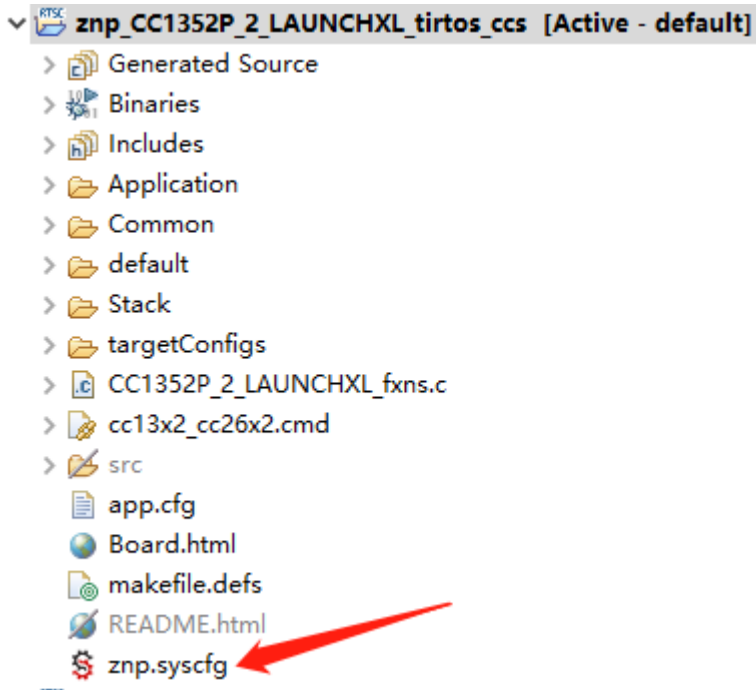
2. Click [Browse] and select the ZNP project file under SDK:



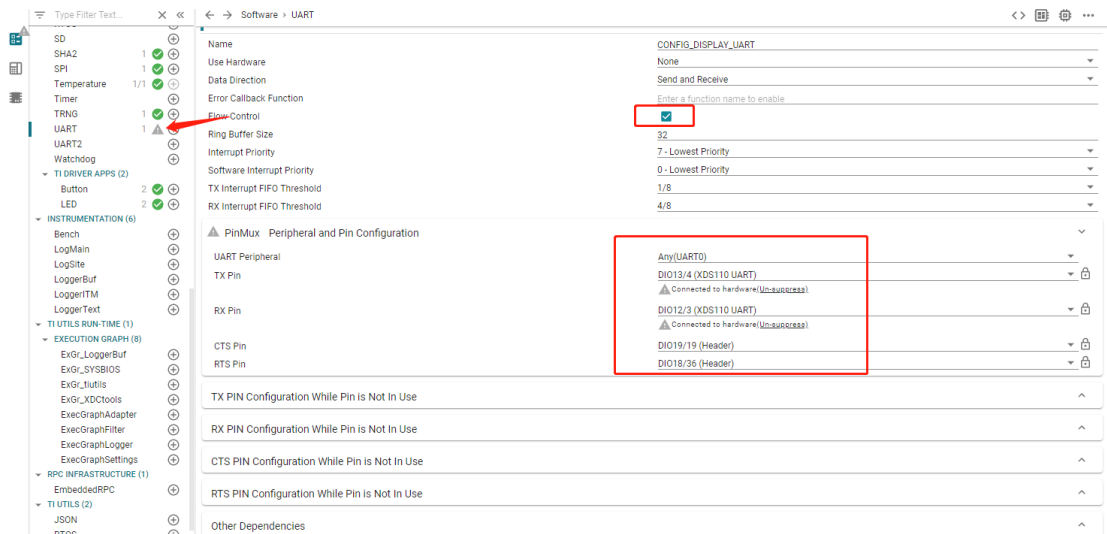
3. Click [Finish]

2.2 Configure engineering hardware flow control

1. Open the .syscfg configuration file in the ZNP project:



2. Enable serial flow control in the UART option in the .syscfg configuration file:



3. Then save and compile.